

ABSTRACT

1 This invention describes a reconfigured form of the PID
2 compensator such that the rate of change of the position error
3 is inherently limited without affecting the performance of the
4 servo loop when the position error is small. The technique
5 described here maintains the performance of the conventional
6 PID compensator when the position error is close to zero,
7 which is the operating point of primary interest.